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SEP 2 8 2004 W	ATTY. DOCKET NO. SP036.C10 (1397.012000A)	APPLICATION NO. 10/713,145
FORM PTO-1449	FIRST NAMED INVENTOR: BRASI	HEARS et al.
Third Supplemental	FILING DATE	ART UNIT
. INFORMATION DISCLOSURE STATEMENT	November 17, 2003	2183

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	AA14						-		
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	AC14								
'n	AD14	5,276	,848	01/1994	Gallagher et al.				
W	AE14	4,901	,233	02/1990	Liptay				
W	AF14	5,134	,561	07/1992	Liptay				
W	AG14	5,619	,730	04/1997	Ando				
6	AH14	5,930	,520	07/1999	Ando				
2	Al14	4,679	,141	07/1987	Pomerene et al.				
~	AJ14	5,418	,973	05/1995	Ellis et al.				
	AK14		· # · _ · ·						
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1/2	AL11	64-36	336	02/07/1989	Japan			x	Y s No
4)	AM11	64-86	243	03/30/1989	Japan			X	Y s No
	AN11	4-153	733	05/27/1992	Japan			×	Yes No
	AO11	2-487	32	02/19/1990	Japan			X	Yes No
(~	AP11	63-19	2135	08/09/1988	Japan			х	Yes No
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FORM PTO-1449  Third Supplemental	FIRST NAMED INVENTOR: BRASH	EARS et al.
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	AP12		OTHER /I	ncluding Auth	or Title Date Re	rtinent Pages, etc.)		No	
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0		AR 24 Tetsuya HARA et al., "Organizations of An Extended Superscalar Processor Prototype Based on the SIMP (Single Instruction Stream/Multiple Instruction Pipelining) Architecture",  Electronic Information Communication Society Technical Research Report CPSY90-38-63, Electronic Information Communication Society, Inc., July 20, 1990, Vol. 90, No. 144, pp 103- 108 (with English language translation, 14 pages).							
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